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	Docket: 3382-55510	App: 09/602,300 FEB
	Applicant: Engstrom et al.	
	Filed: June 23, 2000	Art Unit: 2759 254

## U.S. PATENT DOCUMENTS

Init.*		Number	Date	Name	Class	Sub	Filed
H.P.		5,394,537	2/95	Courts et al.	711	202	
H.P.		5,499,354	3/96	Aschoff et al.	711	129	
H.P.		5,727,178	3/98	Pletcher et al.	711	202	
H.P.		5,606,685	2/97	Frandeen	711	717	
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H.P.		4,688,167	8/18/87	Agarwal	345	803	
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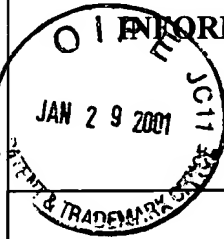
## FOREIGN PATENT DOCUMENTS

		Number	Date	Country	Class	Sub	
H.P.		EP0 713 176 A2	22.05.96	European Patent Appl.			
H.P.		EP0 713 176 A3	22.05.96	European Patent Appl.			

EXAMINER: *haher*

DATE 9/2/03

\*Examiner: Initial if considered, whether or not in conformance with MPEP 60;  
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				Docket: 3382-55510		App: 09/602,300	
				Applicant: Engstrom et al.			
				Filed: June 23, 2000		Art Unit: <del>2759</del> <b>2154</b>	
<b>FOREIGN PATENT DOCUMENTS</b>							
H.P.		EP0 620 523 A2	19.10.94	European Patent Appl.			
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<b>OTHER DOCUMENTS</b>							
H.P.			V. Sohal, "Reliable Memory Management for Real-Time Systems, <i>Electronic Design</i> , Vol. 44, No. 13, June 1996, pp. 118, 120, 122, 124 XP000625394, see page 120, right-hand column, line 7-page 124, left-hand column, line 39				
H.P.			"Packing Variable-Sized Segments in the Swap File of a Paging-Based Virtual Memory System", <i>IBM Technical Disclosure Bulletin</i> , Vol. 39, No. 3, March 1996, pp. 301/302 XP000581702				
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EXAMINER: <u>M. J. [Signature]</u>				DATE <u>9/2/03</u>			
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